

U.S. Department of Energy Energy Efficiency and Renewable Energy

Power Electronic Converters for Advanced Electric Power Systems

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The DOE Workshop on Systems Driven Approach to Inverter R&D Maritime Institute, Baltimore, MD April 23-24, 2003





Outline

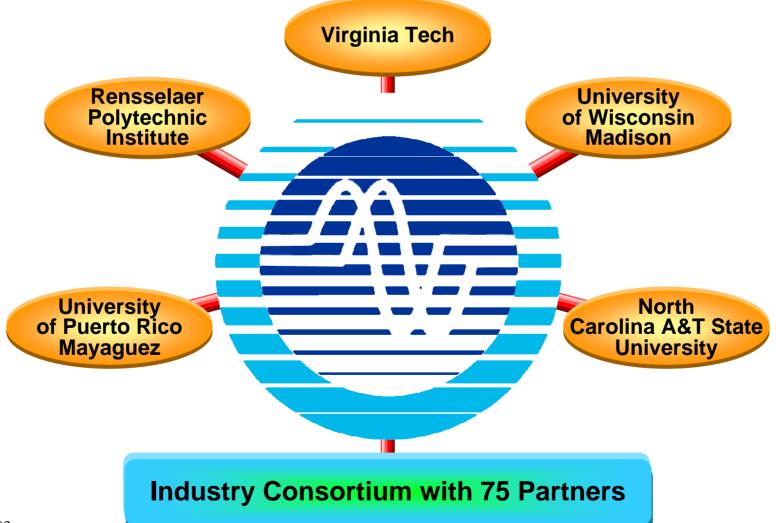
- Introduction
- Source and grid interface issues
- Modular converter design issues
- Standard-cell, open-architecture, electric power conversion systems





Center for Power Electronics Systems

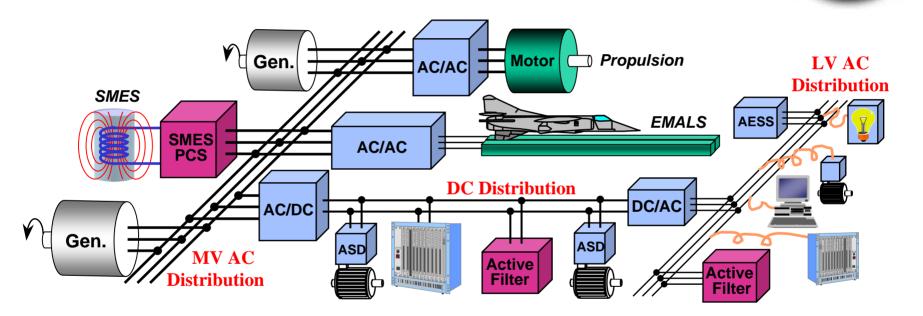
A National Science Foundation Engineering Research Center







Power Electronics Building Block (PEBB) and Advanced Electric Power Systems



• Most (all) sources and loads interfaced through power electronics converters:

- High system controllability, flexibility, and responsiveness
- Increased availability
- Reduced size and weight
- Increased efficiency



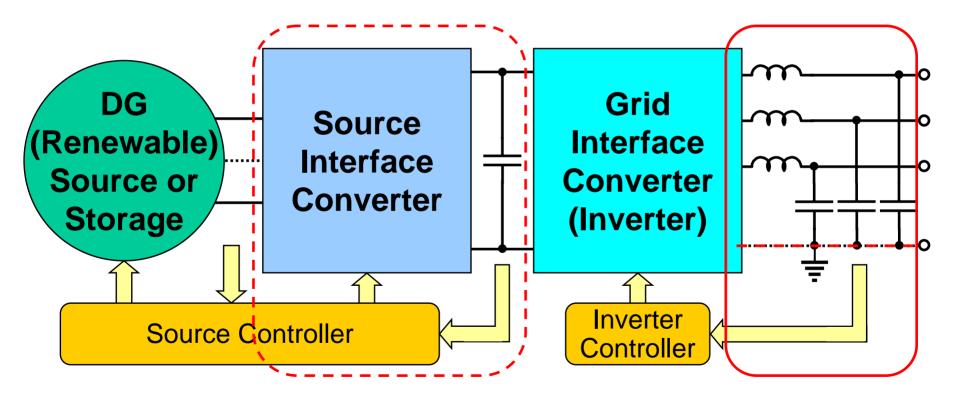
Commercial General Purpose Induction Motor Drives?

- 3-phase, 0-300 Hz, 0-480 V
- 0.2-200 kW (overload 110-150%, surge 150-200%)
- Cost: 200-400 \$/kW
- Size: 0.3-0.6 kW/lit (5-10 W/in³)
- Weight: 0.6-1.2 kW/kg (0.7-1.4 lb/HP)
- Communications: large variety available
- Reliability: 10,000-30,000 hrs MTBF?
- Required modifications:
 - Front-End + DC Link \Rightarrow Source Interface Converter
 - AC Filter + AC Voltage Sensing
 - Control





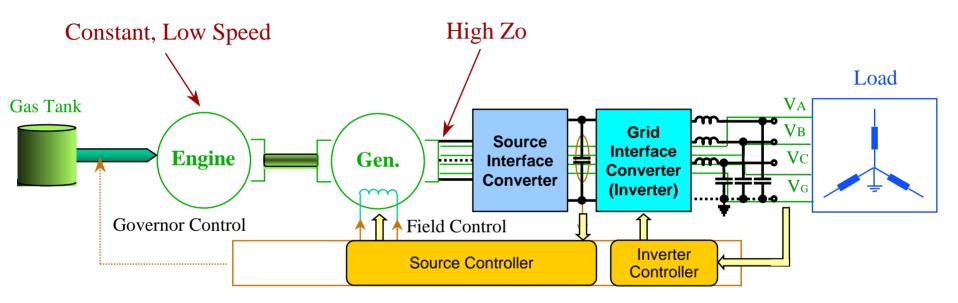
Power Electronics Converters for Distributed (Renewable) Generation







Standard Commercial Gen-Sets

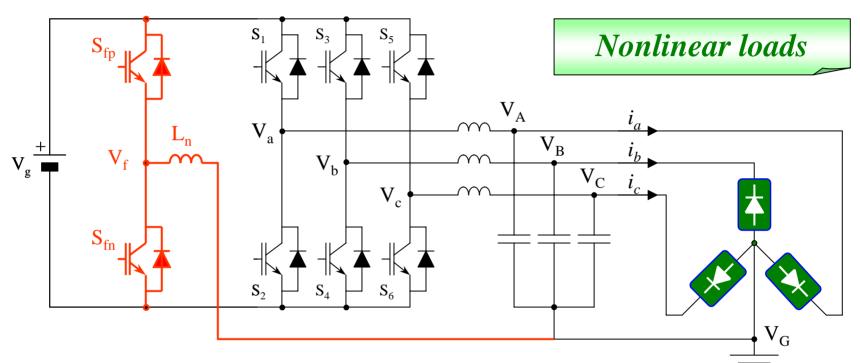


- © Almost unlimited ampere-hour capability
- ☺ Low speed, bulky, high cost engine/generator
- ⊗ Poor transient response
- ☺ High voltage distortion with unbalanced and/or nonlinear load





Unbalanced/Nonlinear Loads



4-Leg Three-Phase Inverter

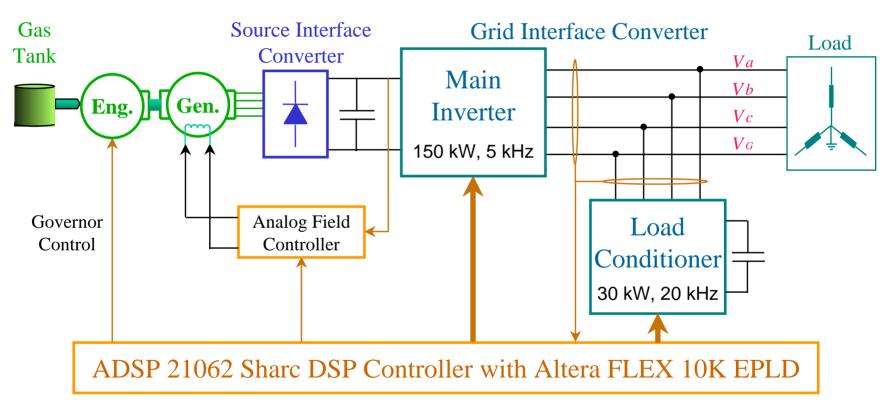
- New space vector modulation
- New control algorithms
- High bandwidth required

Power Electronic Converters for AEPS





Gen-Set with Power Electronics Interface



- Suitable for any variable unidirectional ac source:
 - Micro-turbines
 - Wind power
 - Micro hydro power

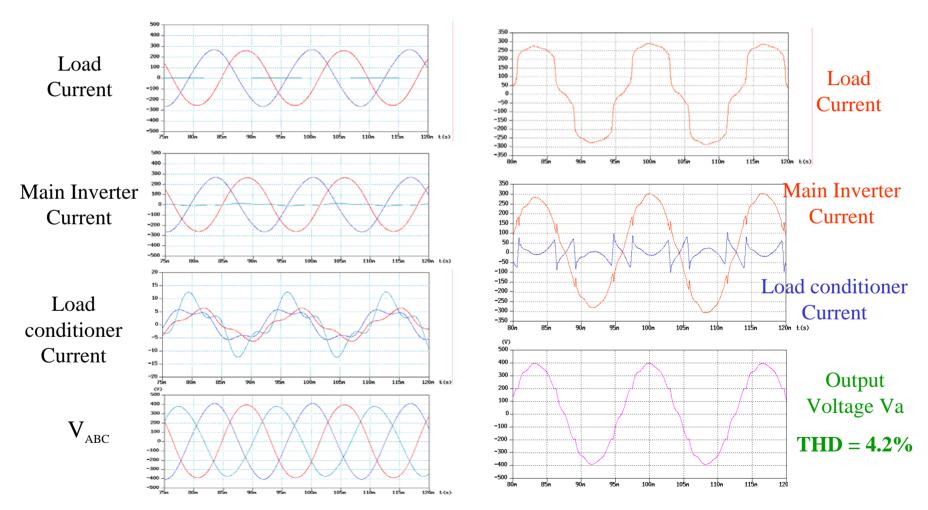






Unbalanced loads

Nonlinear loads

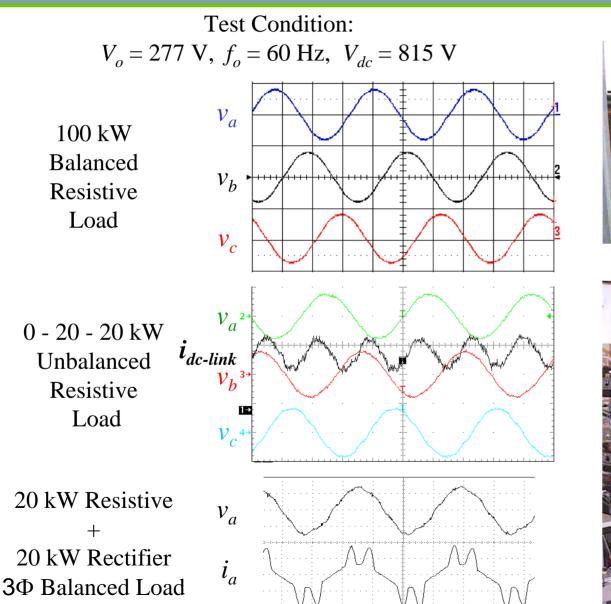




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Experiment

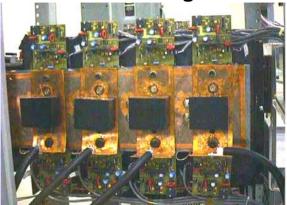




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Power Stage



Hardware Setup

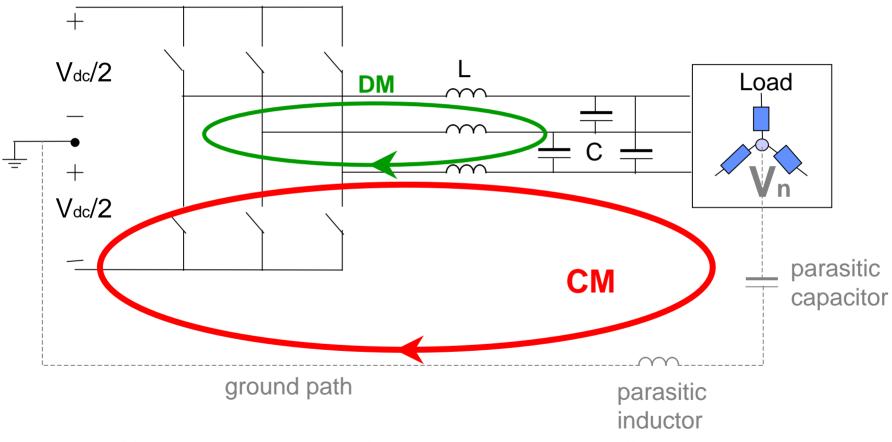


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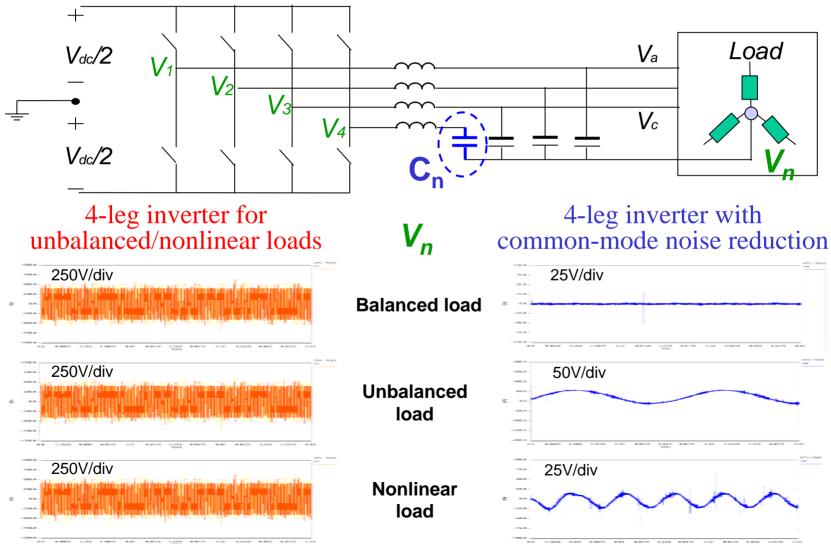
Switching Noise of Inverter System



- Differential-Mode Noise: reduced by LC-filter
- Common-Mode Noise: its effect depends on parasitic values



Active Common-Mode Noise Elimination



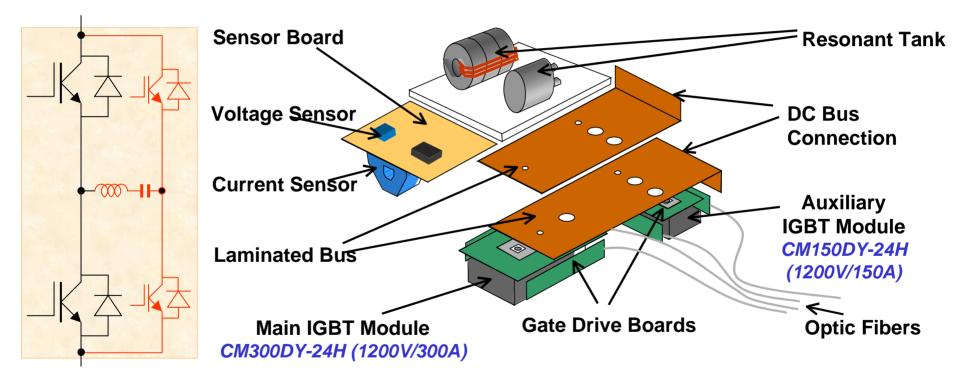
Power Electronic Converters for AEPS





Soft-Switched Phase-Leg PEBB Module

- Increase switching frequency and reduce switching noise
- Increased power density and increased system bandwidth



Zero-Voltage, Zero-Current Transition (ZVZCT) Soft-Switching Cell

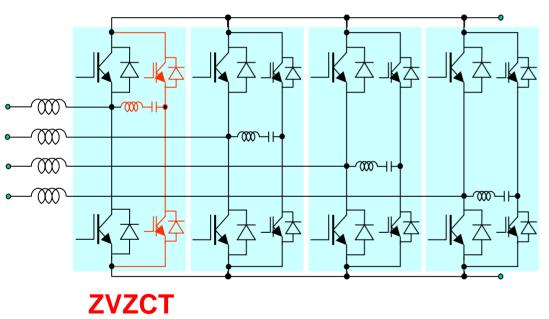
- Main switch zero current turn off
- Main diode zero current turn off
- Auxiliary switch soft switching





Modular, PEBB-Based Converters

100 kW ZVZCT Three-Phase Inverter / Rectifier





Specifications

- AC Voltage:
- DC Voltage:
- Power Rating:
- Switching Freqy:

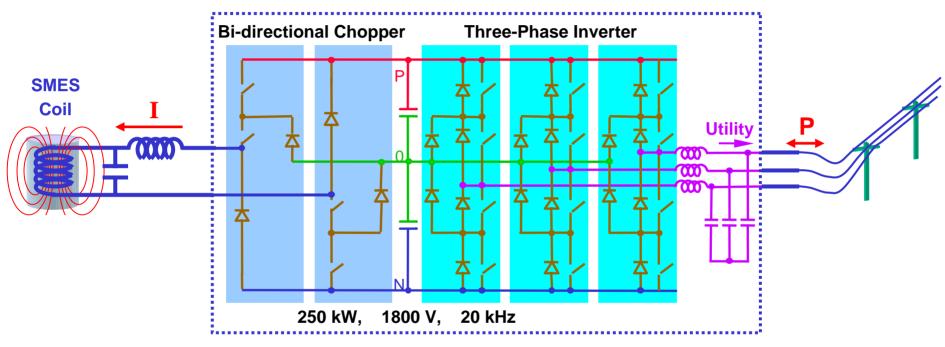
3Ø, 60 Hz, 480 V 800 V 100 kW 20 kHz

PEBB





Power Conditioning System for SMES



- Suitable for any variable dc source:
 - Batteries
 - Solar (PV) cells
 - Fuel cells
- With back-to-back inverter, suitable for
 - Flywheel storage

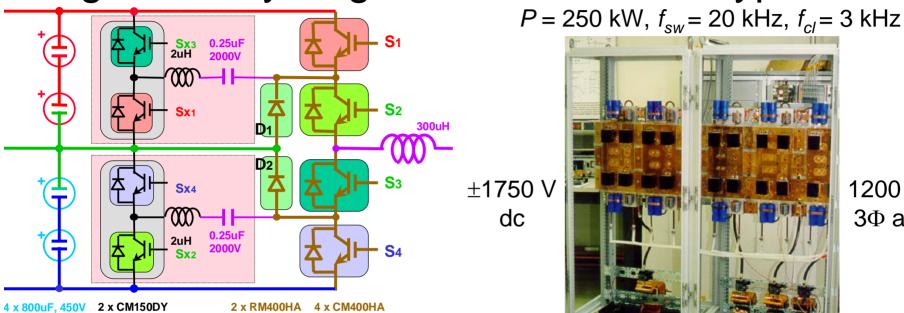
- Energy reserve
- Voltage support
- VAr compensation
- Harmonic filtering

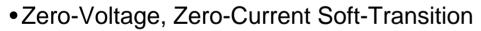


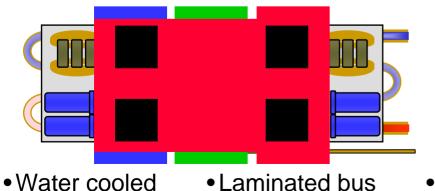
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High-Density, High-Bandwidth Prototype







1200 V 3Φ ac

High power density: 47 W/in³



- Integrated driver
- Optical fiber interface

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Power Electronic Converters for AEPS



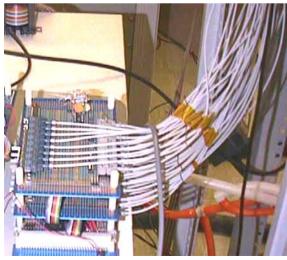
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Modular "Plug & Play" Control Architecture



ZVZCT 3Φ VSI

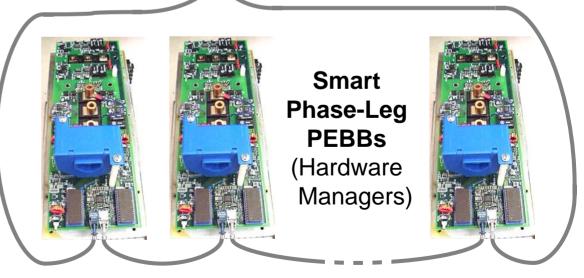


ZVZCT 3Φ three-level VSI

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Universal Controller (Application Manager)

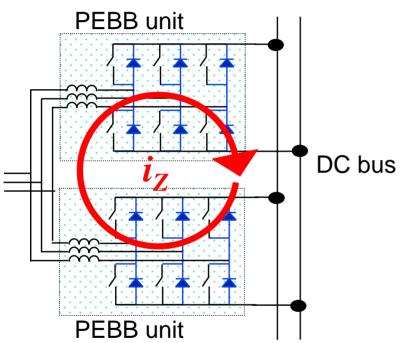


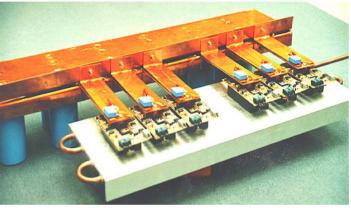
125 Mb/s POF Daisy-Chained Serial Bus (PESNet)



Modular Converter Systems

Example: Parallel Converters





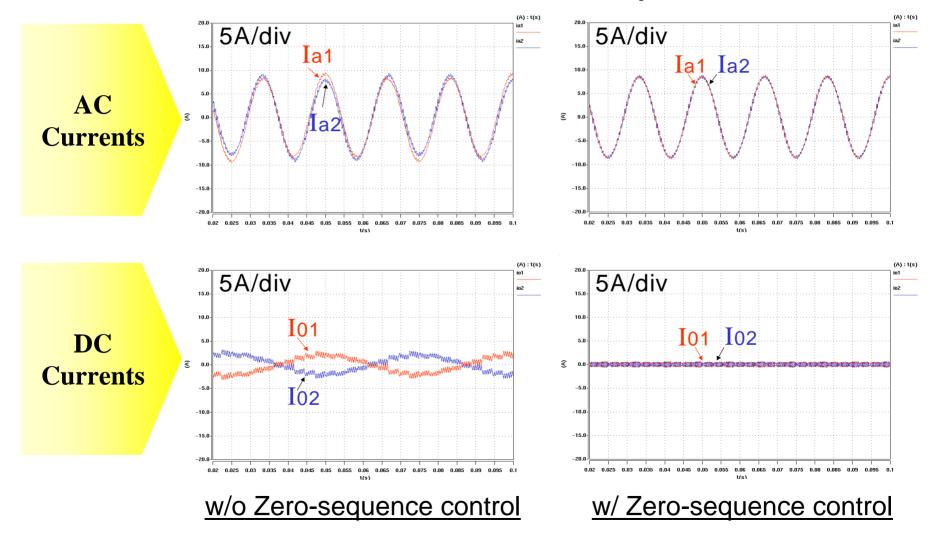
Specifications• AC voltage:208 V• DC voltage:400 V• Power rating:20 kW / unit• Switching freqy:32 kHz

- Zero-sequence current must be controlled in (N) parallel converters
- · For modularity, need independent controllers per converter
- New control algorithm provides for zero-sequence current control by:
 - Small modification in modulation
 - Simple additional (zero-sequence) current controller in N-1 converters





Three-Phase Boost Rectifier Operation

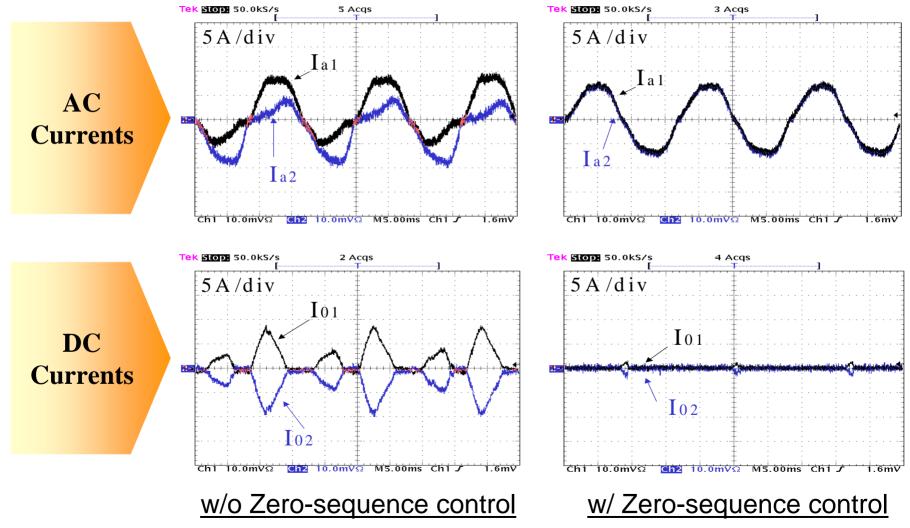


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Experiment



Three-Phase Boost Rectifier Operation



Conceptual Reference Model CPES

Standard-Cell, Open-Architecture, Electric Power Conversion Systems

