Module and Array Testing at the Outdoor Test Facility

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• “Collaborative R&D with industry on module, array, inverter, BOS components, and system requirements related to system design, performance reliability, safety, diagnostics, and failure analyses…”
• “Accredited, unbiased, internationally recognized cell, module, array, inverter, and system performance measurements for U.S. industry…”
• “Coordinated implementation of accelerated testing and long-term field aging of PV arrays from TPP partnerships to determine failure mechanisms, performance degradation rates…”
Relevance/Objectives (cont.)

FY2007 Solar AOP — Module and Array Testing Agreement:

- Indoor accelerated stress testing using consensus standard protocols such as damp heat, thermal cycling, hot spot endurance, and humidity-freeze cycling
- Module mechanical integrity tests such as hail impact, mechanical loading, robustness of wiring terminations, and junction box integrity
- Outdoor stress testing with accelerated solar UV exposure and/or high-voltage bias.
FY2007 Solar AOP — Module and Array Testing Agreement:

- Determination of module energy ratings and outdoor performance degradation rates using the Performance and Energy Ratings Testbed (PERT)
- Technical support and participation with committees (ASTM, IEEE, IEC, UL) developing consensus standards for performance, reliability, and safety of modules
- Standardized module safety testing in collaboration with Underwriters Laboratories, assisting manufacturers to obtain UL certification.
FY2007 Solar AOP — Module Failure Analysis Agreement:

- [Address] module reliability, and thereby system-level performance, through elimination of failure modes that are identified from a failure investigation.
- Attempt to identify the causes of failures observed during testing at the national laboratories, or in modules deployed in the field.
- Diagnostic tools:
  - infrared imaging cameras
  - light and dark I-V measurements
  - quantum efficiency measurements
  - core sampling
  - polymer and adhesion diagnostics
  - surface analysis
  - laser spot scanning.
• Major technical focus is support of PV industry with module and system reliability.
• Most important outcome with regard to cost of PV-generated electricity is determination of module degradation rates.
• Personnel:
## Project Task(s)  
<table>
<thead>
<tr>
<th>Project Task(s)</th>
<th>Total Value</th>
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<tbody>
<tr>
<td>PVB6.7101: Exploratory Reliability &amp; Performance R&amp;D</td>
<td>$1,269K</td>
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<tr>
<td>PVC6.7101: PV System Performance and Standards</td>
<td>$967K</td>
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<tr>
<td>PVB7.7101: Module and Array Testing</td>
<td>$2,240K</td>
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<tr>
<td>PVB7.7201: Module Failure Analysis</td>
<td>$500K</td>
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<tr>
<td><strong>Grand Total</strong></td>
<td><strong>$4,976K</strong></td>
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Accomplishments

Real-Time Outdoor System Testing:

- Currently 11 grid-tied systems monitored at the OTF — x-Si, ribbon-Si, a-Si, CdTe, and CIGSS.
- New Shell CIGSS thin-film system installed (“Eclipse” 80W modules).
- Two new SunPower high-efficiency x-Si systems installed.
Real-Time Outdoor System Testing:

- Quarterly test reports delivered to module manufacturers
- Linear regressions to Performance Test Conditions (PTC, 1000 W/m², 1 m/s wind speed, 20°C ambient), for data blocks one month long
- DC and AC PTC power regressions give PTC inverter efficiency
- System degradation rates in percent per year.
Accomplishments (cont.)

Indoor Module Accelerated Stress Testing:

- Extensive series of thermal cycling and damp heat sequences performed for SunPower
- Thermal cycling and humidity-freeze cycling on Solar Roofing Systems prototype laminate coupons
- Thermal cycling with forward current bias on Advent Solar prototype modules with Emitter Wrap-Through (EWT) Si solar cells.
- Thermal cycling and hot-spot protection performed in support of Underwriters Laboratories (UL) module safety certification.
Indoor Module Accelerated Stress Testing:

- New environmental chamber for damp heat testing of large-area modules procured with FY2006 capital equipment funds

- New qualification/safety tests implemented/upgraded:
  - Ground continuity
  - Bypass diode thermal
  - Hail impact
  - Mechanical load
  - Cut susceptibility
  - Temperature test
  - Robustness of terminations
  - Reverse current overload
  - Accessibility test
  - J-Box securement
  - Wet/dry hi-pot
  - Thermal cycling w/ forward bias.
Performance and Energy Ratings Testbed (PERT):

- 45 module channels in three Raydec Multi-Tracer II electronic loads for continuous maximum power-point loading with periodic I-V curves
- Nearly continuous operation since 1993
- Degradation rates determined with PTC power regressions, comparison report published in 2006 — showed that the common 1% per year rule-of-thumb isn’t unreasonable, but varies greatly between modules and manufacturers.
- Major refit currently in progress — new data acquisition computers installed; new outdoor instrumentation installed; aged and cracked module load wiring being replaced.
Accomplishments (cont.)

Module Energy Ratings:

- Broad research topic — from IEC 61853-style standard day ratings to one-year ratings at specific locations.
- Currently investigating the problems and issues with using PERT data to obtain standard day ratings.
- Analysis of average photon energy (APE) versus air mass has shown that a simple air mass qualifier to model spectral variations is insufficient.
- Research is continuing — will hopefully be used to guide final form of the draft IEC 61853 standard.
Module Failure Analysis:

- New torsional shear measurement of encapsulant materials from module core samples implemented — see poster by Gary Jorgensen and Tom McMahon.
- Long-term study of slow, linear short-circuit current loss in x-Si cells in module packages continues with over 17 years’ equivalent UV exposure.
- Thin-film hot & humid exposure subcontracts — testing at FSEC & TAMU continues; both subcontracts extended through FY2007; exposure data delivered to NREL for archiving and analysis.
Outdoor High-Voltage Stress Test (HVST):

- Exposure test with individual modules held at ±600V WRT the frames.
- Leakage currents measured in real time.
- Looks for possible long-term effects of high-voltage bias, especially corrosion.
- Uses active maximum power-point tracking while array leakage currents are measured; periodic I-V curves.
## Milestones — Module and Array Testing:

<table>
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<tr>
<th>Event Description</th>
<th>Date</th>
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<tr>
<td>Establish T&amp;E Working Group, coordinate lab activities, and provide T&amp;E support for DOE/TPP participants (with Sandia).</td>
<td>9/30/07</td>
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<tr>
<td>Deliver system performance reports &amp; analyses to module manufacturers.</td>
<td>Quarterly</td>
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<tr>
<td>Obtain UL accreditation at NREL for IEC61215 (qualification) and IEC61730 (safety) testing of modules.</td>
<td>9/30/07</td>
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<tr>
<td>Evaluate modules in Performance and Energy Ratings Testbed, replace with new technologies as needed.</td>
<td>5/31/07</td>
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<tr>
<td>Support test and analysis for thin-film, hot &amp; humid module exposure contracts and other in-house and contract research.</td>
<td>9/30/07</td>
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<tr>
<td>Report on feasibility of using PERT data for module energy ratings, IEC standard.</td>
<td>9/30/07</td>
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## Milestones — Module Failure Analysis:

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<tr>
<th>Milestone</th>
<th>Due Date</th>
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<tr>
<td>Continue investigation of $I_{sc}$ degradation in encapsulated c-Si modules.</td>
<td>9/30/08</td>
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<td>Develop diagnostic methods to address failure mechanisms that develop with new prototypes or fielded systems.</td>
<td>9/30/07</td>
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<tr>
<td>Support the requirements of SAI/TPP participants for failure analysis.</td>
<td>9/30/07, continuing</td>
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Future Directions

- Test & evaluation in support of Stage-Gate reviews for SAI TPPs
- Expansion of indoor accelerated stress test capacity — floor space now available at OTF for two new environmental chambers, but depends on increasing building electrical power service
- Increasing capacity for outdoor degradation rate measurements on large numbers of higher-power modules — will require development of inexpensive maximum power point tracking loads
- Increasing rack space and grid capacity for additional small grid-tied systems in the OTF array field.